

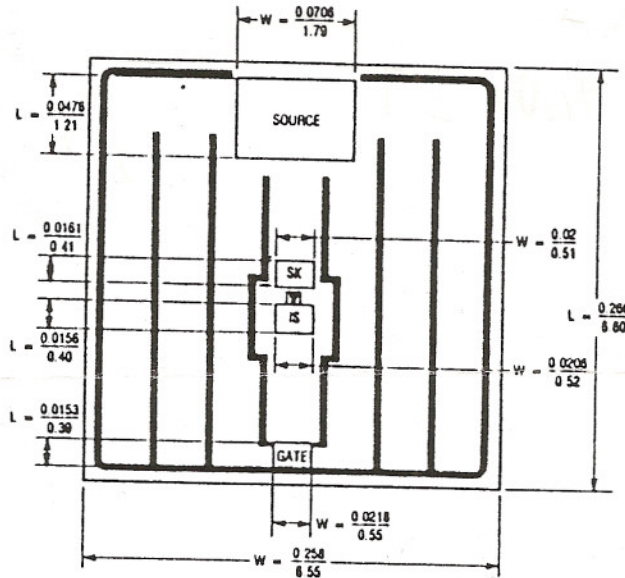


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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



HEXSense-5 100 to 500V,
N-Channel

SK - SOURCE KELVIN
IS - CURRENT SENSE

Topside Metal: Al
Backside:
Backside Potential:
Mask Ref:
Bond Pads : .004"

APPROVED BY: CB

MFG: IR

DIE SIZE : .258" x .260"

THICKNESS: .020"

DATE: 2/7/01

P/N: IRCC450